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**** CONTINUING DATA *******
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 JAPAN 2002-249622 08/28/2002
 JAPAN 2002-352626 04/12/2002

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: <i>[Signature]</i> Initials: <i>ca</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 2	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 5
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TITLE
 Protective structure of semiconductor wafer, method for protecting semiconductor wafer, multilayer protective sheet used therein, and method for processing semiconductor wafer

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